SCF FAPI
Implementation in OAI

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Current state of (n)FAPI in OAI

- **FAPI**: Interoperability with NVIDIA Aerial layer 1 accelerator
  - Uses FAPI 222.10.02 with some exceptions/fixes/backports from 222.10.04
  - Uses nvipc library (shared memory)
- **nFAPI**: Limited interoperability between OAI VNF and OAI PNF
Towards OAI (n)FAPI interface code as the accepted reference definition Stage 3 implementation for the SCF specs

SCF
- FAPI v6 Specs
  - Extracted parameters and attributes (e.g., .csv file)
- FAPI v7 Specs
  - Extracted parameters and attributes (e.g., .csv file)

Mechanical Conversion

OAI
- (n)FAPI C(++) API headers, constructors, and parsers
- OAI Open FAPI Repo
  - Test cases and tools

O-RAN
- O-RAN Specs (e.g., AAL)

V6 seed code (.h files) contributed by Qualcomm
Wishlist

➢ Layer 1 inline accelerator based on FAPI v6
➢ Support to implement FAPI v6 in OAI layer 2